



ACADEMIC & RESEARCH
ALLIANCES

SolvNetPlus Quick Guide

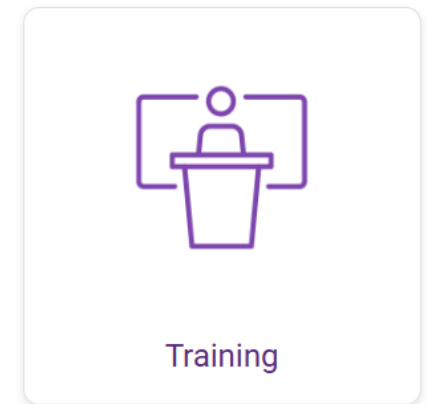
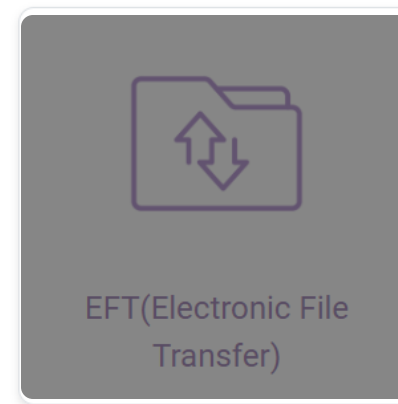
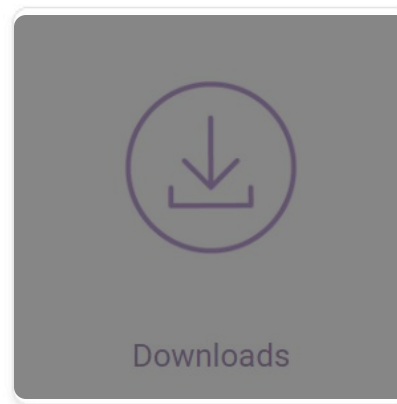
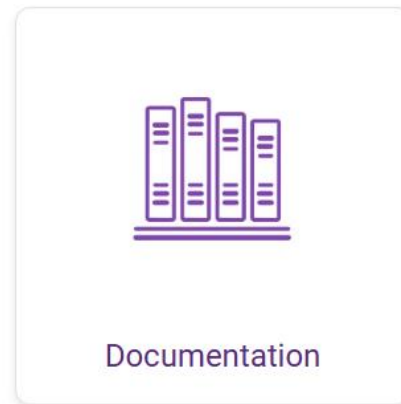
Knowledge Base Targeting

Region: Taiwan

April 2026

What is SolvNetPlus

- [SolvNetPlus](#) is the suite of support services Synopsys delivers via the internet.
- Services include:



Currently, software downloads and EFT functions are not available to university members of the Knowledge Base

Policy - Eligibility



Who **CAN** Access?

- Authorized university members
- Received valid **Site ID**
- Active affiliation with the TSRI University Software Program (Professor)



Who **CANNOT** Access?

- Examples:
- Visiting researchers
 - Contractors / Guests / Alumni
 - **Individuals not formally affiliated with the university**



University Responsibility

- Universities must enforce **Synopsys's terms of use**
- Access must comply with the institutional license
- **Additional References:**
 - [SolvNetPlus Terms of Use](#)
 - [University End-User Software License and Maintenance \(EULM\) Agreement](#)

Policy - Regulation



Email Requirement

- Official university email domain required (.edu.tw)
- Public email domains are **not allowed** (e.g. Gmail, Hotmail, yahoo, 163, ieee)



No Sharing of Accounts

- Accounts are individual
- **Must not be** shared, even within the same university
- Users will be audited on a regular basis



Proprietary Content Restrictions

- Distribution of proprietary materials is strictly prohibited
- Applies to electronic & hard copy formats

Application Flow – Taiwan

TSRI Membership & Software Request

1

2

3

- The professor must first apply for TSRI membership
- Once approved, the professor needs to request Synopsys software tools via the TSRI website
- The software license agreement must be signed by professor

⚠ This step must be completed before submitting the application

Application Flow – Taiwan

1

*Application Form
Submission*

2

- Complete the attached application form
- The professor must sign under the “**Approval**” section
- Send the signed application form to sara-tw@synopsys.com
- The submission will be reviewed and verified by Synopsys

[Application Form \(TAIWAN\).pdf](#)

3

Application Flow – Taiwan

1




2

Account Information
& Access

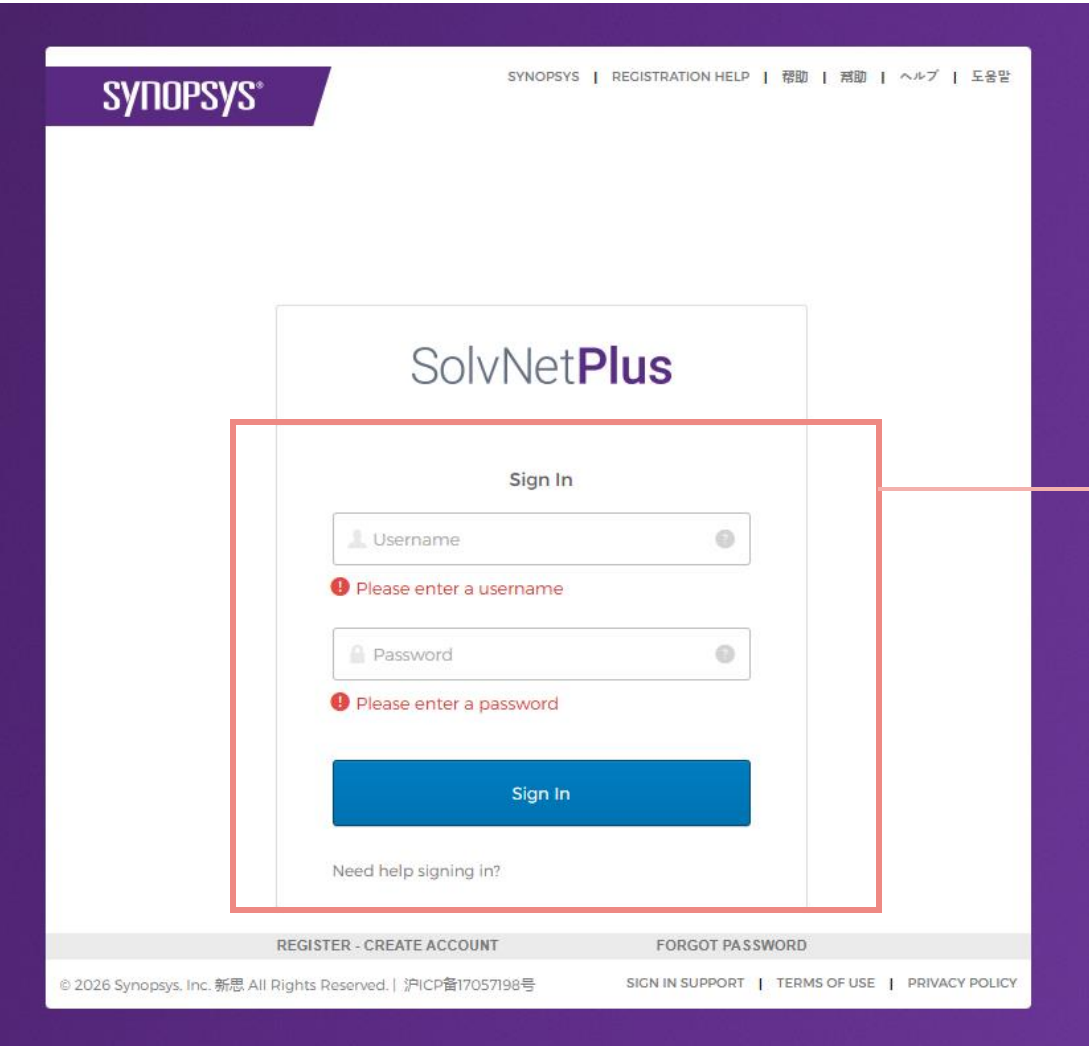
3

- Once approved by SARA-TW, a **confirmation email** will be sent to each applicant
- The email will include instructions for accessing
- The whole process needs **7-10 working days**

Your SolvNetPlus Access is Approved
Please kindly find below for log-in information

Step 1 Click Below Registration Link	Step 2 Complete Personal Information	Step 3 Create your account using Site ID:
		

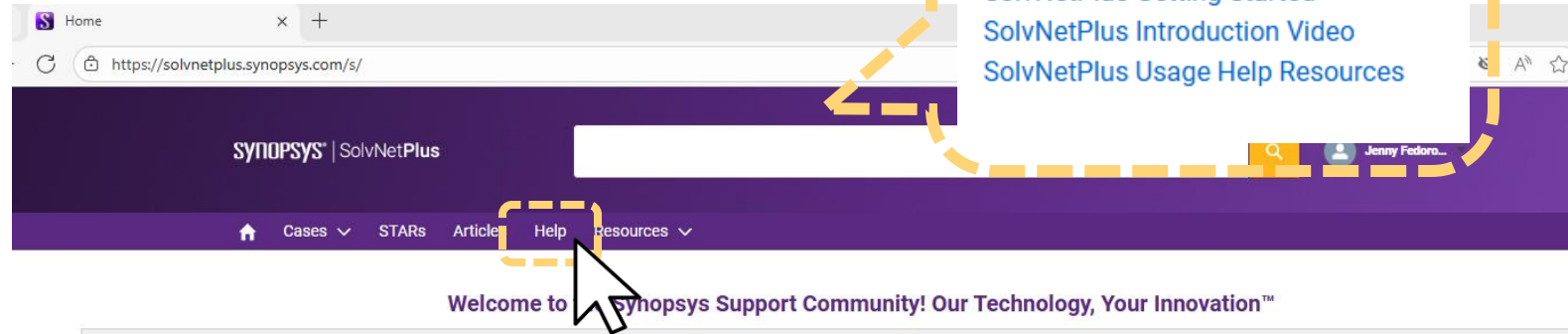
Login



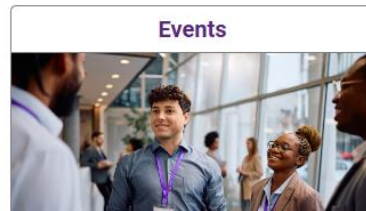
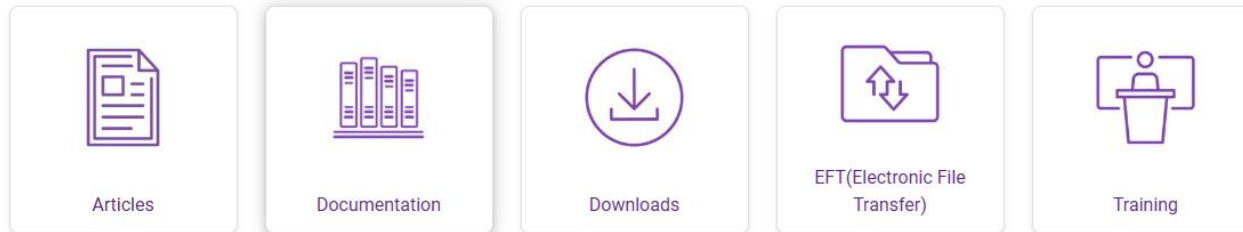
Log-in with SolvNetPlus credentials



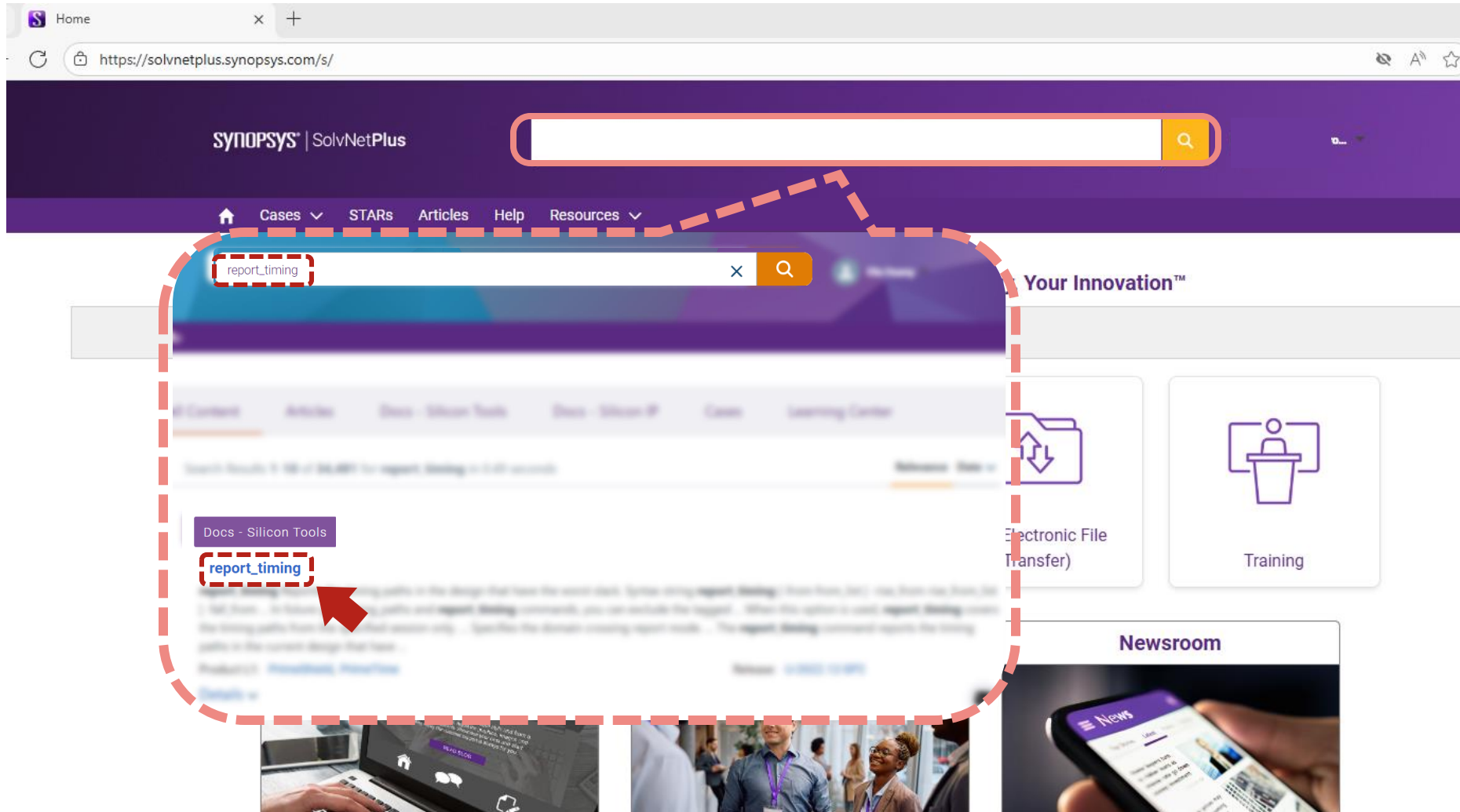
GET STARTED



By clicking on **Help** button, you can access tutorials for using SolvNetPlus and various resources needed during usage.



GET STARTED: Search Bar



Advanced search engine retrieves information from various sources, such as documentation, articles, training, etc. It does not provide information from Cases & STARs.

Key Features: ARTICLE



ARTICLE

- **Top Trending Articles** and **Recently Published Articles** are populated on the home page.
- You can click on “**View All Articles**” to access all the related articles for your product selection

Top Trending Articles

Filter by Products:

Title	Product L1	Views
Online Training	SolvNet	220896
Short Recordings	SolvNet	77733
Reference Methodology Archives	Reference Metho...	45198
How Are Clock Gating Checks Inferred...	PrimeTime	44634
PrimeTime Parametric On-Chip Variat...	PrimeTime	34667
Product Update Trainings	Formality	33325
VCS Documentation, P-2019.06-SP2	VCS	27621
Tcl Training	PrimeTime	26486
IC Compiler II Reference Methodolog...	Reference Metho...	25015
Synopsys Power Management - Support...	Power Compiler	24865

[View All Articles →](#)

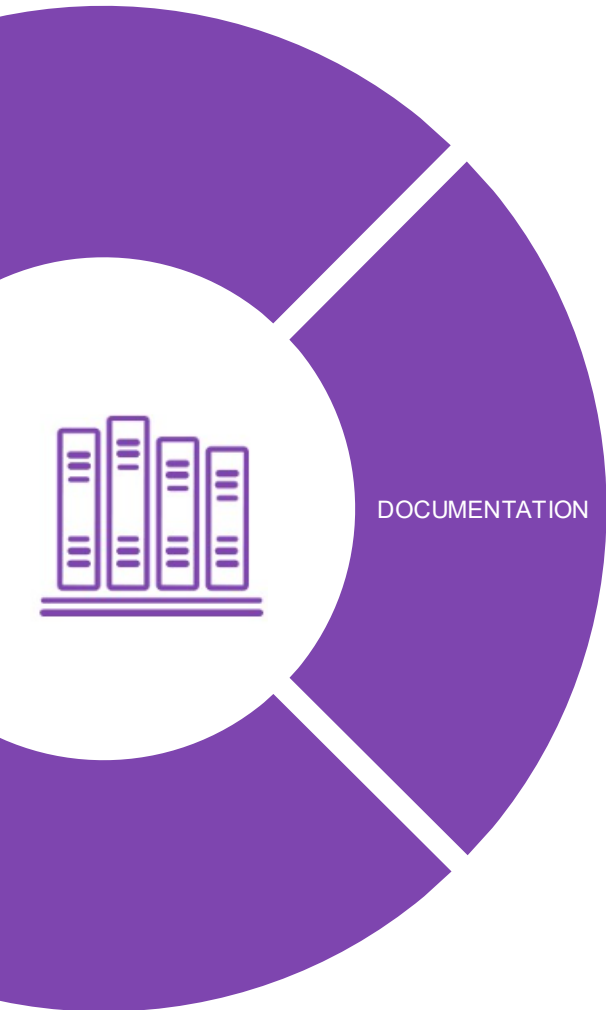
Recently Published Articles

Filter by Products: Time Period:

Title	Product L1
Toward Greener Data Centers: AI-Assisted...	OptoCompiler
How to Debug Thermal Setup Before Runnin...	3DIC Compiler
Understanding CHECK_CONCAT_MAX_WIDTH Beh...	VC SpyGlass
DYNAMIC-CLOCK-VIOLATION ...changed by U...	VCS
How to Run IR Analysis With analyze_pg C...	3DIC Compiler
Layout Only Waveguides and Connectors En...	OptoCompiler
How to Set Up Top and Bottom Layers of L...	3DIC Compiler
How to Launch External Machine for RedHa...	3DIC Compiler
Starting, stopping, and rereading the Sy...	SCL - Synopsys ...
Basic Flow to Check Fusion-to-StarRC/Pri...	Fusion Compiler

[View All Articles →](#)

Key Features: DOCUMENTATION



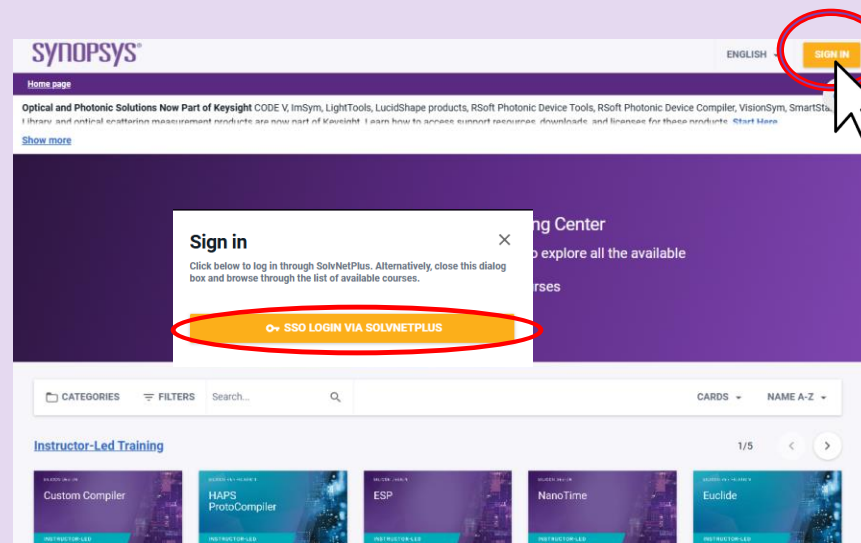
- Documentation contains product release notes, installation guides, user guides & reference manuals.
- You can search by product name to get tool documents.
- Can also download release notes, installation guides, user guides, and reference manuals from this section.



Key Features: TRAINING



- Synopsys Learning Center provides free self-paced training resources.
- Login to SolvNetPlus then click “Training”, you will be redirected to [Synopsys Learning Center](#)



1. Click the “SIGN IN” at the top right corner.
2. Click “SSO Login Via SolvNetPlus Account”.

Synopsys Learning Center



University Program

Synopsys product courses and resources intended for learners from universities that are part of Synopsys' University Program

My Dashboard

Your learning paths and progress in one convenient dashboard

Access

Learning Journeys

Structured paths to help you navigate the learning content

Explore

A guide to product-based and role-based learning paths.

Self-Paced Courses for University

Browse through a wide selection of courses

Explore

Flexible, on-demand e-learning modules that include videos, labs, quizzes.

University Curriculum

Enable your classroom with an array of materials for IC design and EDA

Explore

Comprehensive curriculum for Bachelor and Master Programs in microelectronic design and EDA development. Course materials can be used to implement a new course or to supplement content in an existing course.

Libraries, PDKs, Memory Compilers, Foundry-Sponsored IP Products

[Generic Libraries - SolvNetPlus](#)

Enable students to master advanced design methods for low power, IoT, and automotive applications using the latest Synopsys EDA tools.

[Interoperable PDKs - SolvNetPlus](#)

Enable students to master the design of analog and mixed-signal ICs and IPs using the latest Synopsys Custom Implementation tools. Each PDK includes documentation and design infrastructure elements.

[Generic Memory Compiler - SolvNetPlus](#)

Available for academic use when custom tailoring memory circuits for specific design needs.

[Reference Methodology Retrieval System - SolvNetPlus](#)

RMgen provides an easy way to configure and download product-specific and release-specific reference methodology scripts. These are a starting point for developing product-specific flow scripts. Customize the scripts to work in your design environment.

[Foundry-Sponsored IP products](#) are provided free of charge for all University Customers, subject to approval by the Foundry via [the respective SolvNetPlus process](#).

Resources

- [SolvNetPlus](#)
- [SolvNetPlus Terms of Use](#)
- [University End-User Software License and Maintenance \(EULM\) Agreement](#)
- [Application Form \(TAIWAN\).pdf](#)

THANK YOU

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sara-tw@synopsys.com