



## Synopsys Academic & Research Alliances (Taiwan) – FAQs

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Synopsys Academic & Research Alliances (SARA) proprietary documents can be used for teaching and fundamental research purposes:

*“Fundamental research means basic and applied research in science and engineering, the results of which ordinarily are published and shared broadly within the scientific community, as distinguished from proprietary research and from industrial development, design, production, and product utilization, the results of which ordinarily are restricted for proprietary or national security reason.”*

### **Q: What resources and tools are available to support use of the Synopsys tools at universities?**

A: Synopsys offers various materials and tools to support universities, including:

- Curriculum: Over 100 courses for IC design and EDA development.
- Generic Libraries: 14nm, 32/28nm, and 90nm libraries for advanced IC design.
- Interoperable PDKs: For designing analog and mixed-signal ICs.
- Generic Memory Compiler: Software for generating SRAMs.

For more resources and information, please refer to the content on SARA webpage (<https://www.synopsys.com/academic-research/university.html>). These resources are primarily for educational purposes and require a SolvNetPlus ID for access.

### **Q: What resources and tools are available to support use of the Synopsys tools at universities?**

A: The current SARA tool bundle is available at Taiwan Semiconductor Research Institute (TSRI). It is updated regularly by Sales Operations. For more information, please visit the TSRI website (<https://www.tsri.org.tw/main.jsp>). (Route: Chip Implementation > Service Descriptions)



**Q: Are university students eligible to access SolvNetPlus?**

A: Yes, students who wished to access SolvNetPlus should get approval from their professors prior to the applicants.

For more application instructions please visit SARA TAIWAN website:

<https://sara.synopsys.com.tw/StaticPage/22>

**Q: How to apply a SolvNetPlus account?**

A: SolvNetPlus application steps:

1. The professor must first apply for TSRI membership. Upon acceptance, they should request Synopsys software tools on the TSRI website and sign the software usage agreement. (Ensure this step is complete before submitting the application form to Synopsys)
2. Fill out the application form and have **each applicant** sign the attached declaration form.
3. After filling out all forms, have the professor sign at the Approval section at the bottom form. Scan and email it back to [sara-tw@synopsys.com](mailto:sara-tw@synopsys.com) along with any electronic files for review and verification by Synopsys.
4. Once approved by Synopsys University Program, a confirmation email will be sent to the applicant, providing SolvNetPlus account login instructions.

Please refer to the application instructions on SARA TAIWAN website:

<https://sara.synopsys.com.tw/StaticPage/22>

**Q: Do university SolvNetPlus users get technical support?**

A: Yes and no. While university users do not have direct access to file cases on SolvNetPlus for Synopsys technical support, they can request support through TSRI (<https://www.tsri.org.tw>). From there the inquiries are screened and routed accordingly.



**Q: How can I use the SolvNetPlus materials appropriately adhering to the terms of use?**

A: The distribution of Synopsys proprietary documents (electronic and had copy) on campus must be strictly controlled. EVERY university professor, student, or staff member who has access to the Synopsys proprietary documents are responsible for protecting these proprietary materials. By signing the “SARA Taiwan Application Form” and activating the SolvNetPlus account, the users acknowledge that –

- They understand that the proprietary materials (electronic and hard copy) are the property of Synopsys.
- They understand the proprietary materials supplied through SolvNetPlus must be viewed only by themselves and not shown to any other person.
- They understand the proprietary materials may not be copied to or stored in any medium (paper or electronic) that is always not under strictly secure control.
- They understand that any act of posting or redistribution of proprietary materials from SolvNetPlus is a violation of academic integrity, and that legal liabilities may ensure from such an act.

**Q: Can university SolvNetPlus users access training resources on Synopsys Learning Center?**

A: Yes. University SolvNetPlus users can access “Self-Paced Learning” resources on Synopsys Learning Center, with most features available at no cost.

For more features information, please visit below website:

<https://www.synopsys.com/blogs/chip-design/synopsys-learning-center.html>

**Q: Can I use SolvNetPlus contents and Synopsys tools for my research and publications?**

A: Yes, we highly encourage universities to conduct fundamental research and publish papers using SolvNetPlus contents and Synopsys tools. But please mention Synopsys name and the tools used in the reference section of your paper, using appropriate trademarks from Synopsys Trademarks



(<https://www.synopsys.com/company/legal/trademarks-brands.html>). If your research is funded by a company with potential commercial benefits, please contact SARA Taiwan ([sara-tw@synopsys.com](mailto:sara-tw@synopsys.com)) for further clarification.